

UNIVERSITY OF SOUTH FLORIDA

Major Research Area Paper Presentation

3 3:00 PM - 3:45 PM

3:45 PM - 4:30 PM

In this talk, I will discuss our work that focuses on the design and implementation of a novel framework for the automatic synthesis of hardware accelerators for deep learning applications. This work is part of a larger effort to develop a high-level synthesis (HLS) framework that can automatically generate efficient hardware accelerators for a wide range of deep learning applications. Along with this paper, I will also briefly discuss my current work along with a timeline for completing my PhD.

Dr. Shaun Canavan
9068 6888

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THE PUBLIC IS INVITED

Shaun Canavan, Ph.D., Major Professor
Sudeep Sarkar, Ph.D.
Paul Rosen, Ph.D.
Alison Salloum, Ph.D.
, Ph.D.

Xinming Ou, Ph.D.
Associate Chair for Graduate Affairs
Computer Science and Engineering
College of Engineering

3:45 PM - 4:30 PM

Disability Accommodations:

3:00 PM - 3:45 PM : 399 9068 6888 4